

[illegible]

Fig. 1

ORDER

SYMBOL	—	204
SIDE	—	206
QUANTITY	—	208
MPID	—	210
TIF	—	212
PRICE	—	214

PRICE — 214

106

Fig. 2

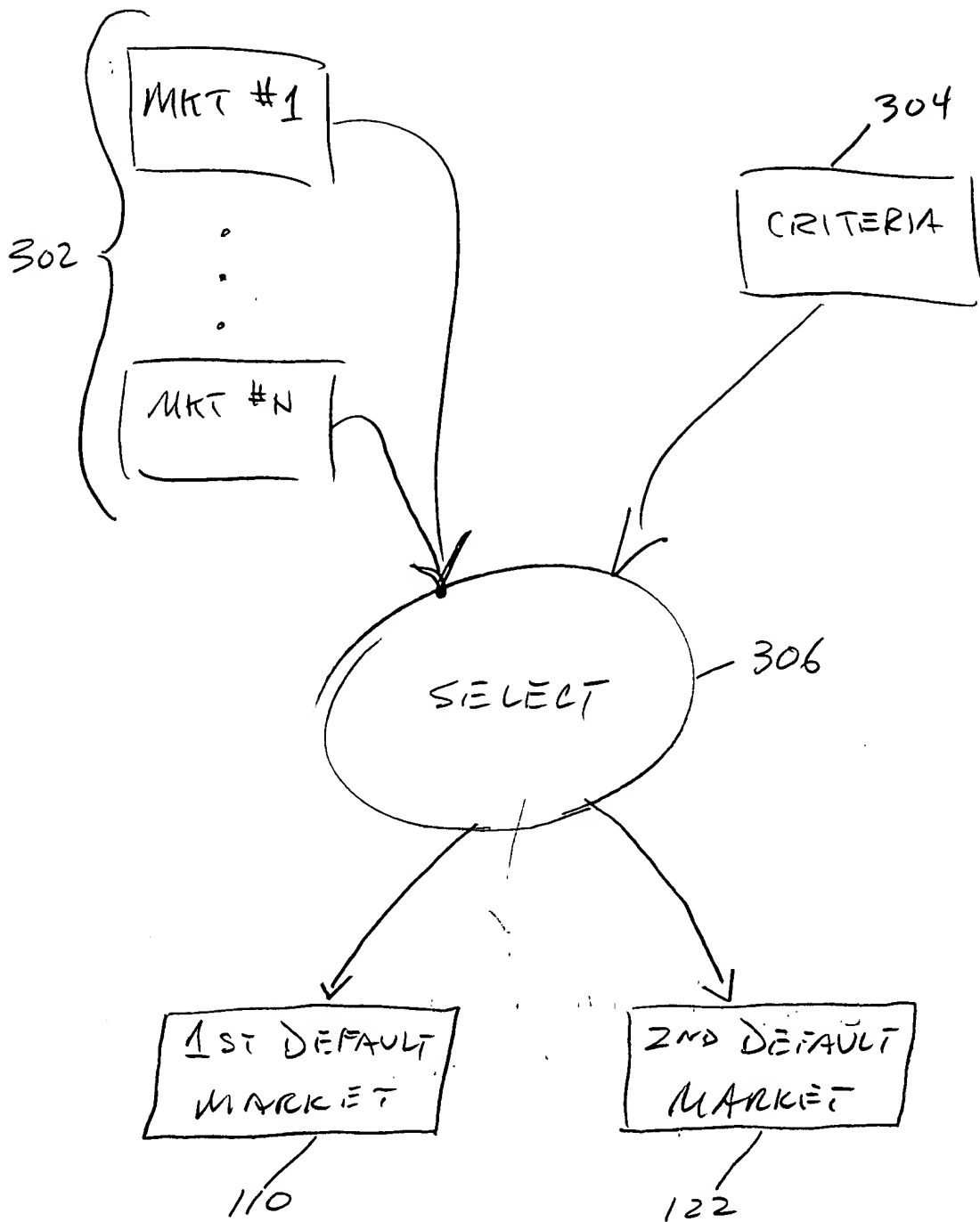
[illegible]

Fig. 3

002200 2224960

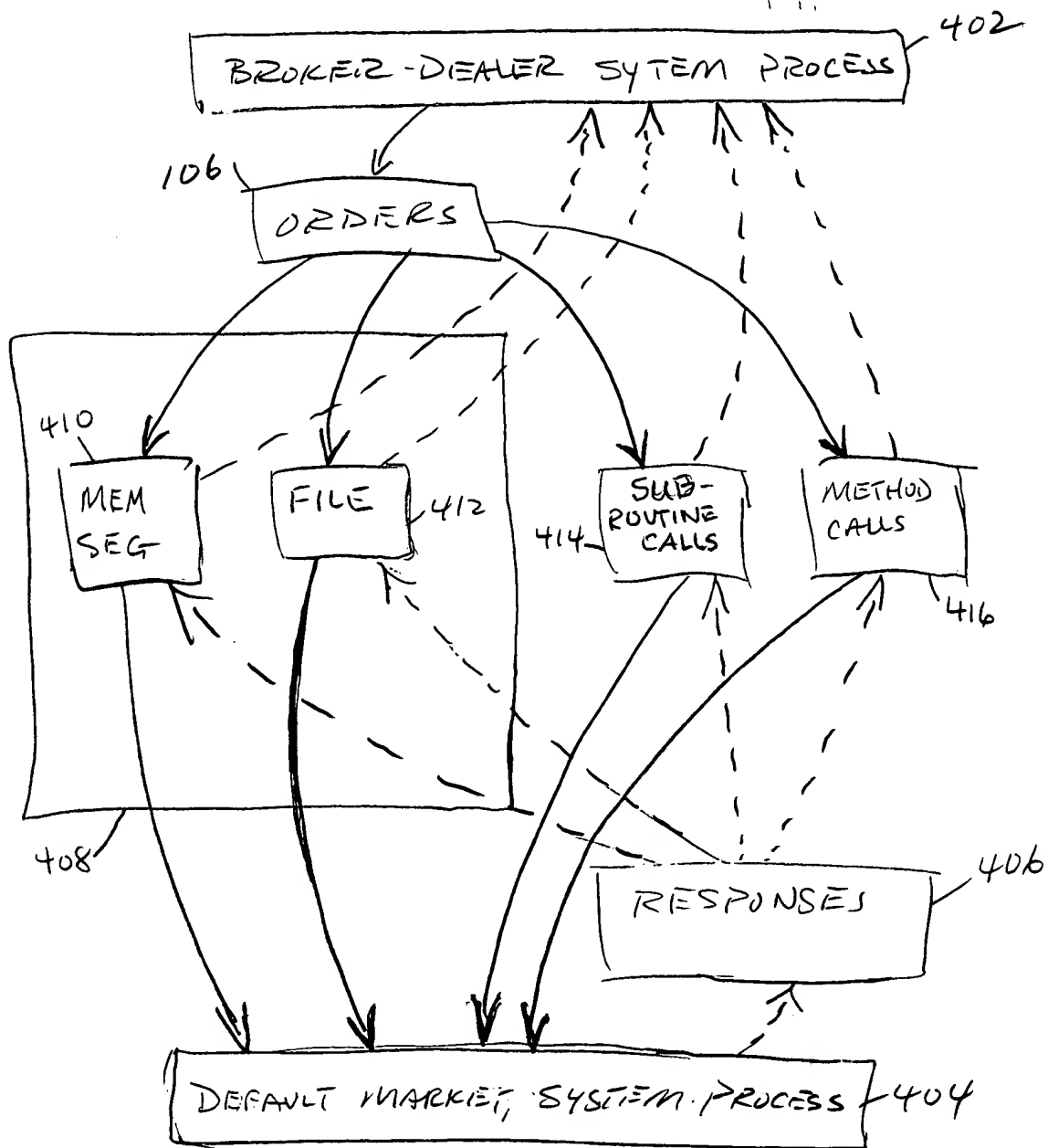


Fig. 4

SECRET

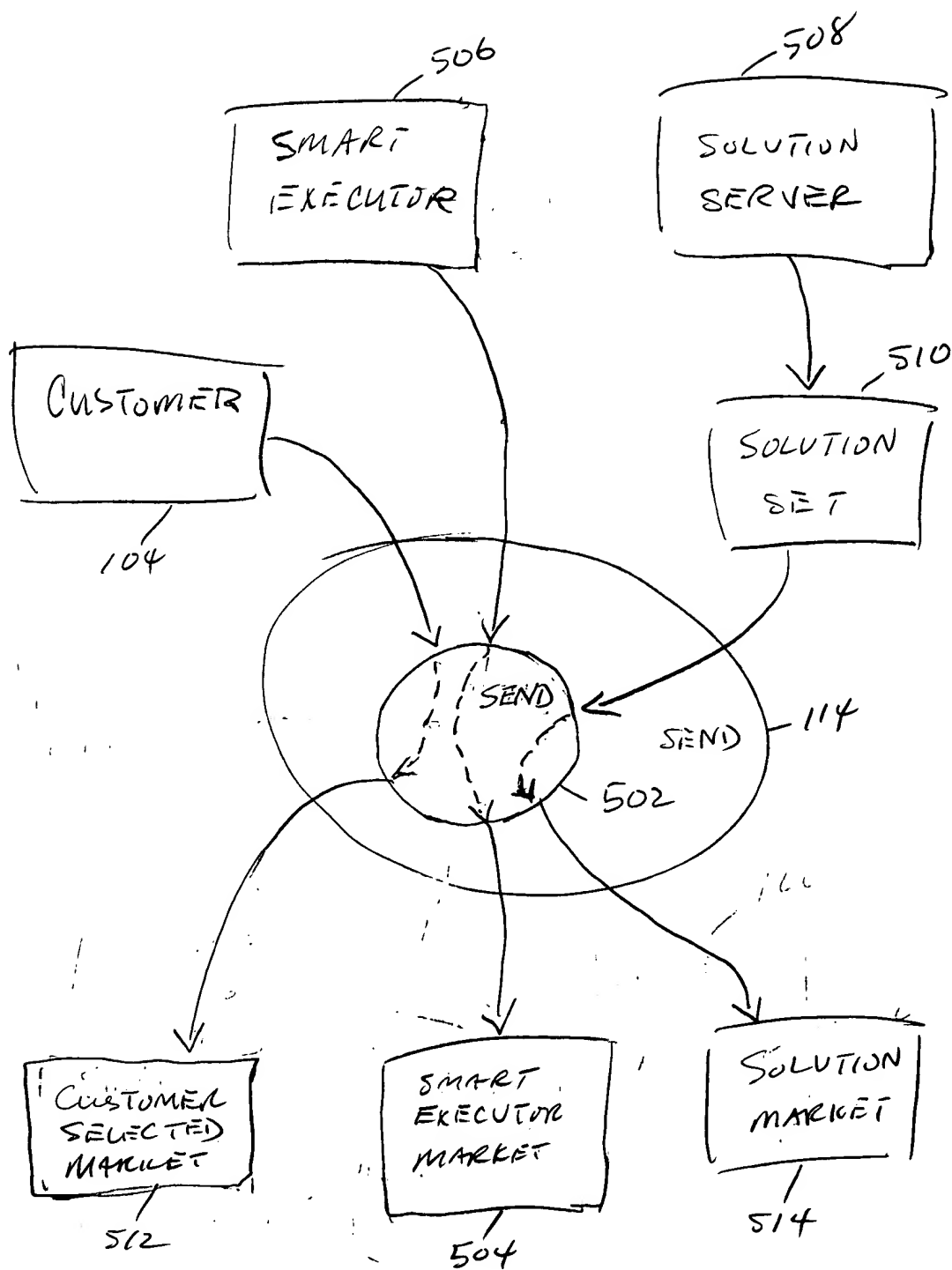


Fig. 5

002280" 2222+960

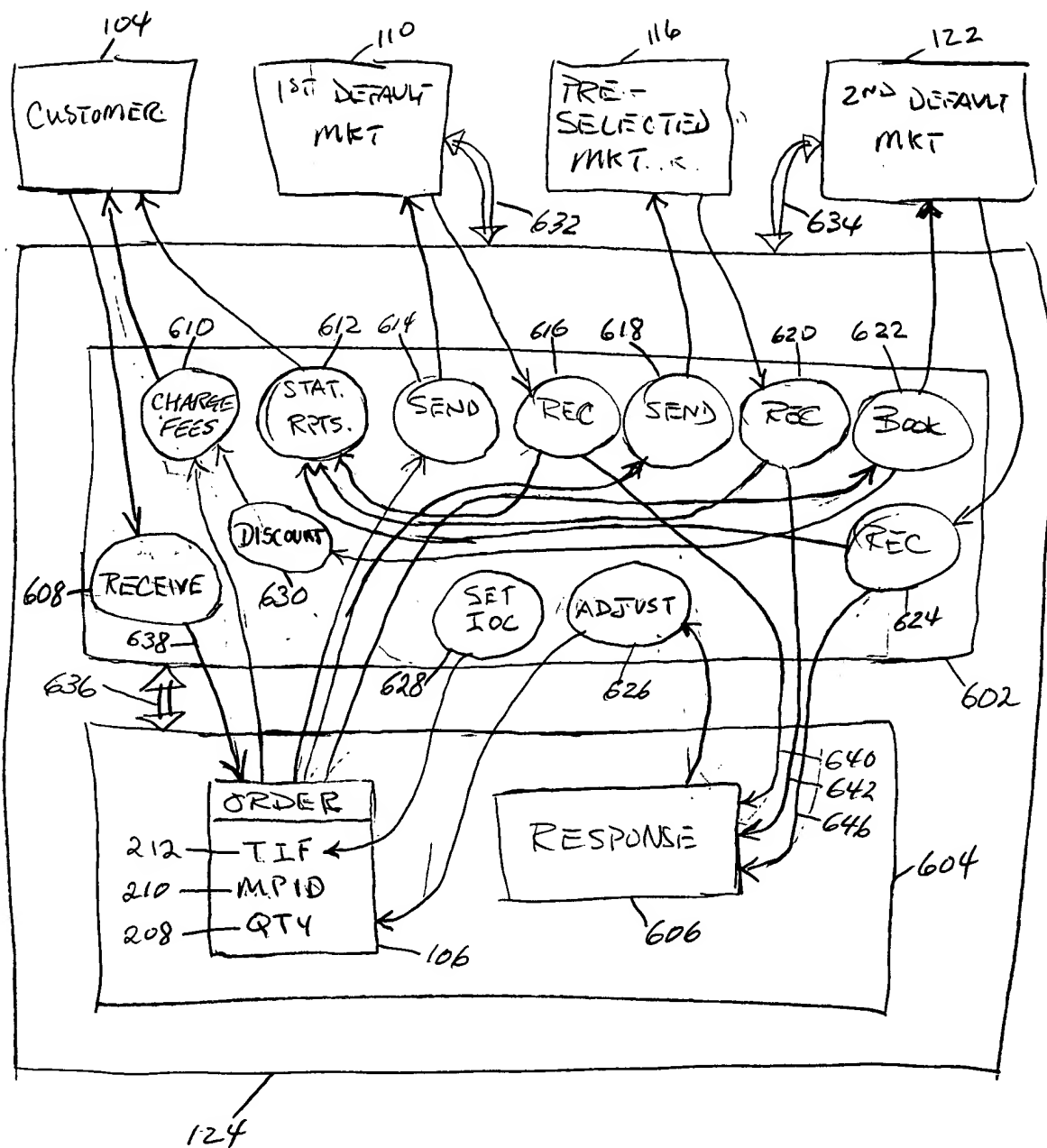


Fig. 6

097608

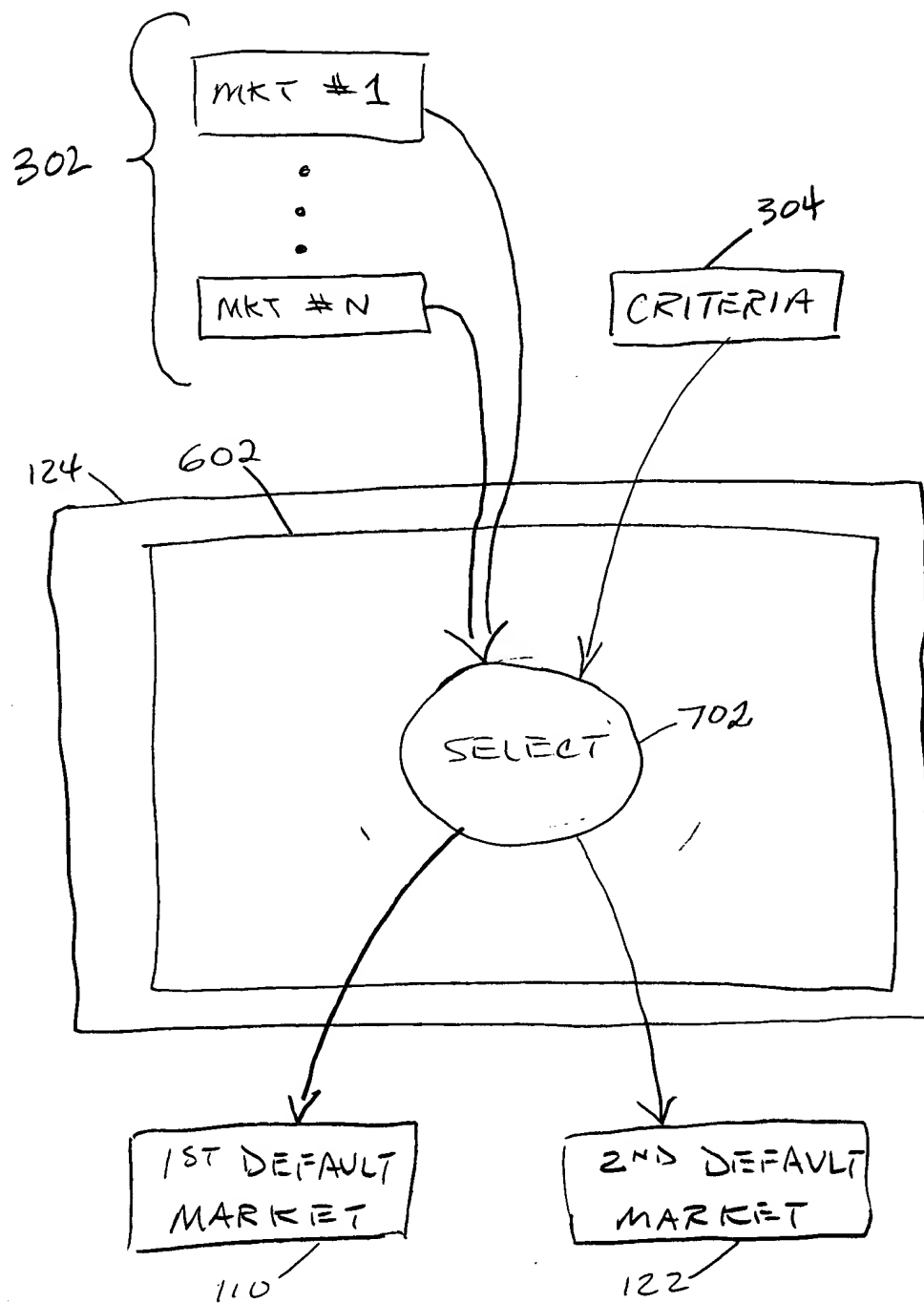


Fig. 7

The diagram illustrates the architecture of a Broker-Dealer System, organized into three main horizontal sections connected by a central bus (830).

- Top Section (800):** Contains the **BROKER-DEALER SYSTEM PROGRAM** (402) and **MEMORY** (604). It is connected to the central bus via a bidirectional arrow (834).
- Middle Section:** Contains the **BROKER-DEALER PROCESSOR** (602), which is connected to the bus via a bidirectional arrow (832). Below the processor are several data and processing blocks:
 - ORDERS** (106): Receives input from the bus (830) and sends data to **FILE** (820), **SUB-ROUT. CALL** (822), **METH. CALL** (824), and **RPC** (826).
 - FILE** (820): Receives data from **ORDERS** and sends it to **MEM. SEG.** (818).
 - SUB-ROUT. CALL** (822): Receives data from **ORDERS** and sends it to **MEM. SEG.** (818).
 - METH. CALL** (824): Receives data from **ORDERS** and sends it to **RPC** (826).
 - RPC** (826): Receives data from **METH. CALL** and sends it to **CORBA** (828).
 - MEM. SEG.** (818): Receives data from **FILE** and **SUB-ROUT. CALL**, and is connected to the bus via a bidirectional arrow (816).
 - CORBA** (828): Receives data from **RPC** and sends it to **RESPONSES** (606).
- Bottom Section (801):** Contains the **DEFAULT MARKET PROCESSOR** (806) and **DEFAULT MARKET SYSTEM PROGRAMS** (802) in **MEMORY** (804). The processor is connected to the central bus via a bidirectional arrow (812).

Arrows indicate the flow of data and control signals between components, with dashed lines representing specific data paths or control signals.

Fig. 2

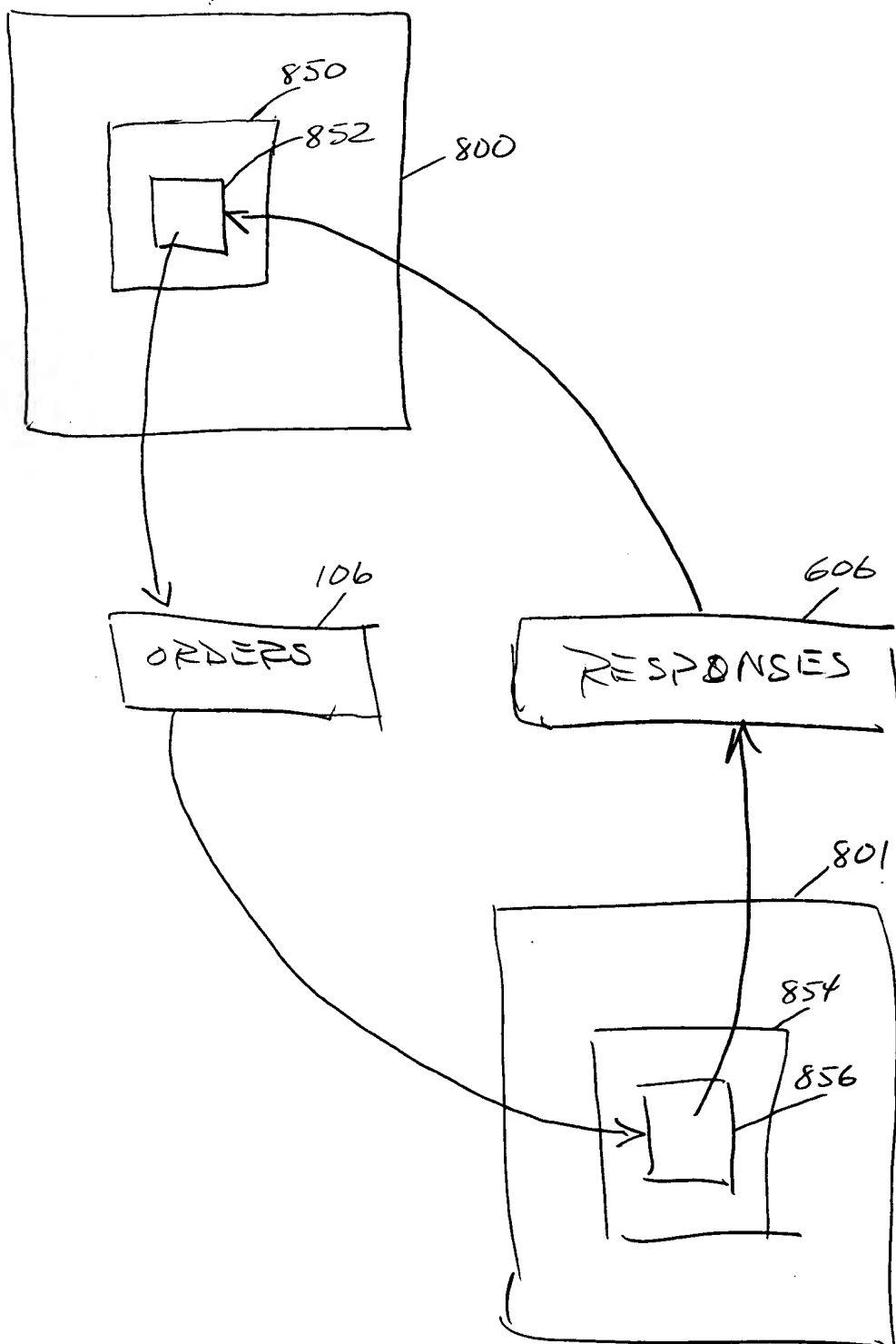
[illegible]

Fig. 8A

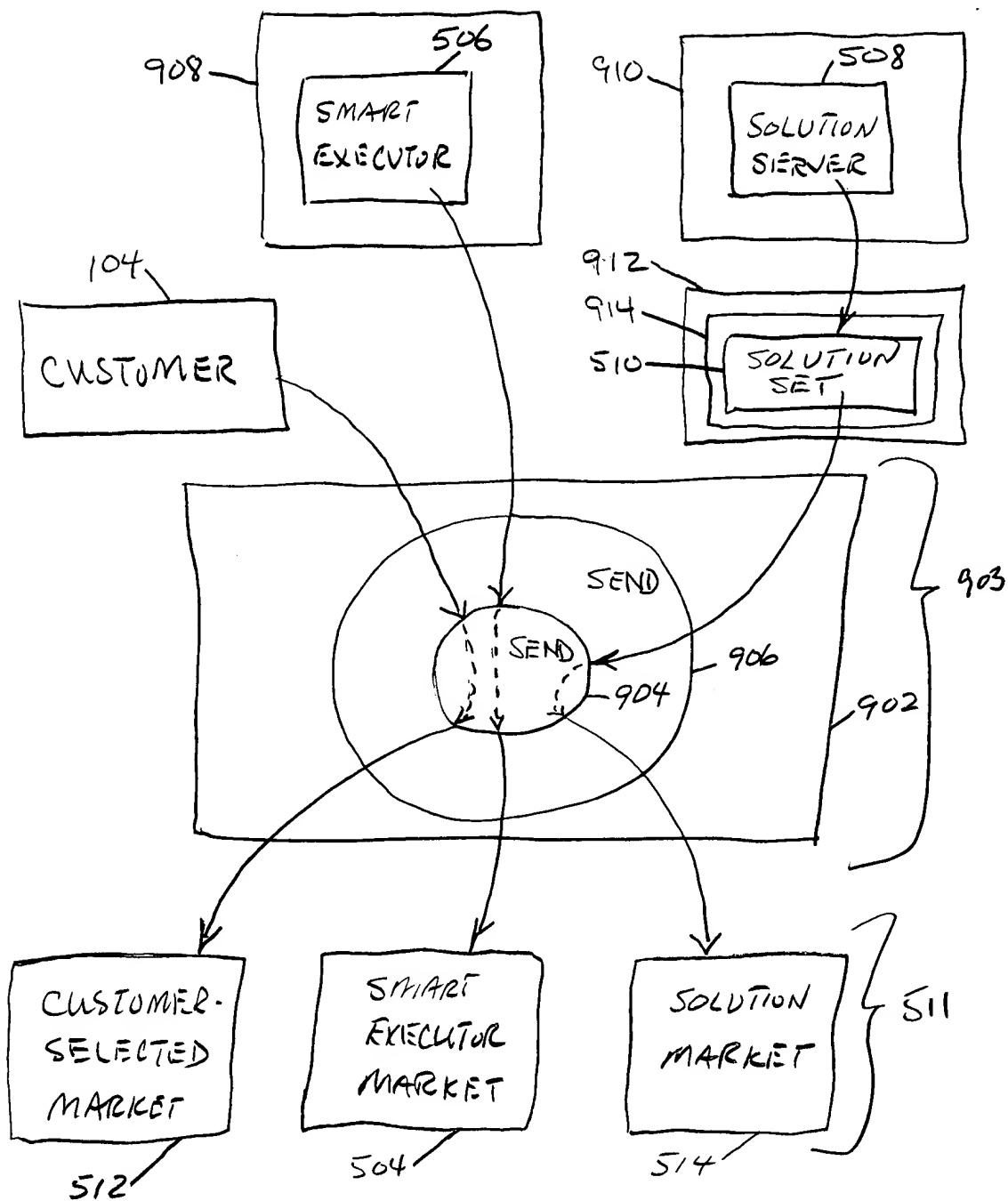


Fig. 9